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Application No. 10/777,752
Amendment dated December 26, 2006
Reply to Office Action of September 25, 2006

Docket No.: 4444-0137P

REMARKS

Claims 1-19 remain present in this application. Claims 2, 5 and 11-19 are currently withdrawn from consideration

The specification and claim 1 have been amended. Reconsideration of the application, as amended, is respectfully requested.

Amendments to the Claims

Independent claim 1 has been amended for clarity, and now recites "forming a thin film transistor on said first glass substrate." It is respectfully submitted that no new matter has been added.

Rejections under 35 USC 103

Claims 1, 3, 4 and 8-10 stand rejected under 35 USC 103 as being unpatentable over Gyoda, U.S. Publication 2002/0063842, in view of Niiya, U.S. Publication 2003/0137630. This rejection is respectfully traversed.

Claims 6 and 7 stand rejected under 35 USC 103 as being unpatentable over Gyoda in view of Niiya, and further in view of Park et al., U.S. Patent 6,628,365. This rejection is respectfully traversed.

Gyoda discloses a method for manufacturing an electro-optical device, which includes a sealing member for bonding a pair of opposing substrates. The manufacturing method includes forming an uncured sealing member having no injection port, by applying an uncured adhesive in a loop configuration in a peripheral portion of each substrate formation region of one of the

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pairs of substrate base materials. Next, an electro-optical layer is fabricating by applying an electro-optical material in the internal area surrounded by the uncured sealing member in each substrate formation region of the substrate base material. Next, an electro-optical cell base material is assembled by bonding the one substrate base material to the other substrate base material with the uncured sealing member. Next, the uncured sealing member of the electro-optical cell base material is cured. Finally, electro-optical cell base material along each substrate formation region is diced.

As set forth in independent claim 1 of the present application, the thin film transistor (TFT) and the black matrix are formed on the first glass substrate, while the sealant is formed on the second glass substrate. In other words, the thin film transistor and the black matrix are formed on same substrate, while the sealant is formed on another. In contrast, Gyoda teaches forming the thin film transistor and the black matrix on different substrates. The Examiner's attention is drawn to Fig. 1 of Gyoda and corresponding paragraph [0083], which clearly shows that *"the top surface of the substrate 11 (facing the liquid crystal layer 13), a number of pixel electrodes 15 and a plurality of TFTs 10 (not shown in FIG. 1)... are formed in the internal area of the sealing member 14."* The Examiner's attention is also drawn to paragraph [0084], which sets forth that *"On an opposing surface, a color filter layer 16 having red (R), green (G), and blue (B) color pixels 16a and a light shield layer (a black matrix) 16b, a common electrode 17, and an alignment layer 19 are successively laminated on the underside of the counter substrate 12 (facing the side of the liquid crystal layer 13)."*

As is discussed in the paragraph beginning on page 4, line 1, *"the ultraviolet rays can not penetrate the black matrix 14 and an opaque thin film transistor 13 formed on the second*

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substrate 12 to irradiate the sealant 16." In addition, as can be seen in Fig.1 of the present application, the prior art teaches that the black matrix and TFT are formed on different substrates. If the black matrix and TFT are formed on different substrates, then the curing will be incomplete, as discussed in the "Description of the Prior Art" of the present application, no matter what substrate on which the sealant is formed.

In view of the foregoing amendments and remarks, it is respectfully submitted that prior art utilized by the Examiner fails to teach or suggest the method of independent claim 1 and its dependent claims. Accordingly, reconsideration and withdrawal of the 35 USC 103 rejections are respectfully requested.

Conclusion

Favorable reconsideration and an early Notice of Allowance are earnestly solicited.

Because the additional prior art cited by the Examiner has been included merely to show the state of the prior art and has not been utilized to reject the claims, no further comments concerning this document are considered necessary at this time.

In the event that any outstanding matters remain in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.


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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Dated: December 26, 2006

Respectfully submitted,

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